Fig. 1

(BACKGROUND ART)

<u>100</u>

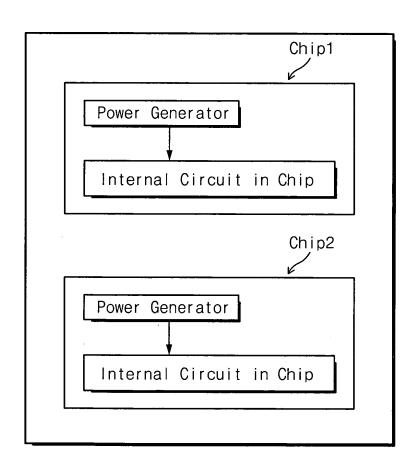


Fig. 2A

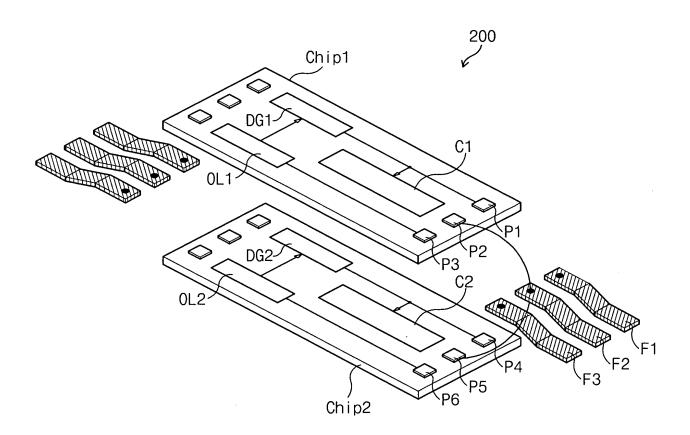


Fig. 2B

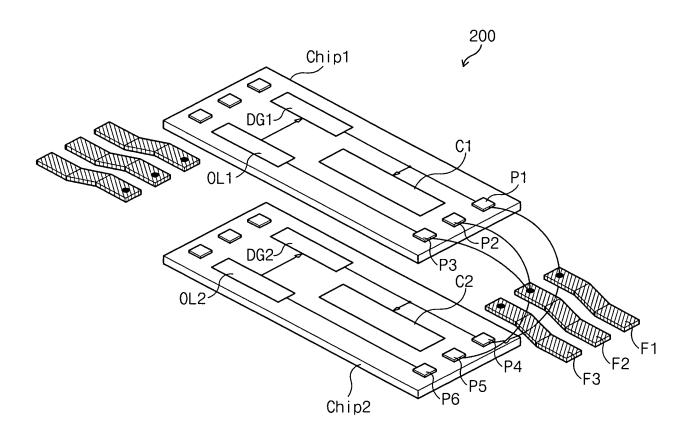


Fig. 3A D63 Chip3~ Chip1 ~ Chip2~

L

~300 **P** /P8 Fig. 3B D62 D63 Chip1 ~ Chip3~ Chip2~

Vss 400 2/19 200 <u>E</u> P4 P6 P1 Chjp2 Chip1 Internal Circuit in Chip Internal Circuit in Chip Fig. 4A — DG2 C 061 0,2 0L1 Selecting Circuit Selecting Circuit Power Generator Power Generator

-20

400 Vss ± 2 € 1 44 <u>P3</u> P6 E E Chjp2 Chip1 Fig. 4B Internal Circuit in Chip Internal Circuit in Chip <-- DG2 C 100 → 0<u>L</u>1 0L2 Selecting Circuit Selecting Circuit Power Generator Power Generator

-20

8